

Quadrature phase Shift Keying QPSK

- * In QPSK the phase of the carrier is varied into 4 discrete levels
- * each level carries two bit of informations
- * The equation which describes the QPSK system is given by

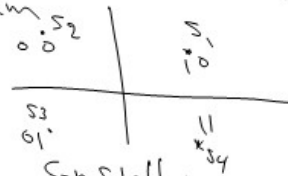
$$S_i(t) = \sqrt{\frac{2E}{T}} \cos[2\pi f_c t + (i-1)\pi/4] \quad i=1,2,3,4$$

E is the symbol energy

T is the symbol duration

$f_c = \frac{n}{T}$, where n is an integer

- * If we plot the constellation diagram for QPSK signal, we may get the following diagram.



- * The constellation diagram can be used to derive an expression for the average probability of error, which is given by

$$P_e = \frac{1}{2} \text{erfc} \sqrt{\frac{E_b}{N_0}}$$

Why we call QPSK quadrature modulation?

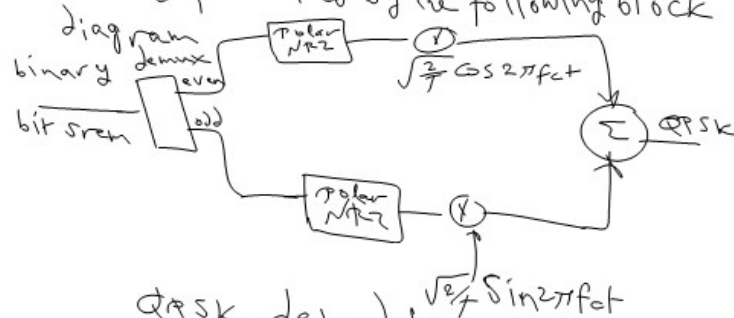
- * The answer to this question came from $s_i(t)$ equation

$$S_i(t) = \sqrt{\frac{2E}{T}} \cos(2\pi f_c t + (i-1)\pi/4)$$

$$= \sqrt{\frac{2E}{T}} \cos(i-1)\pi/4 \cos 2\pi f_c t - \sqrt{\frac{2E}{T}} \sin(i-1)\pi/4 \sin 2\pi f_c t$$

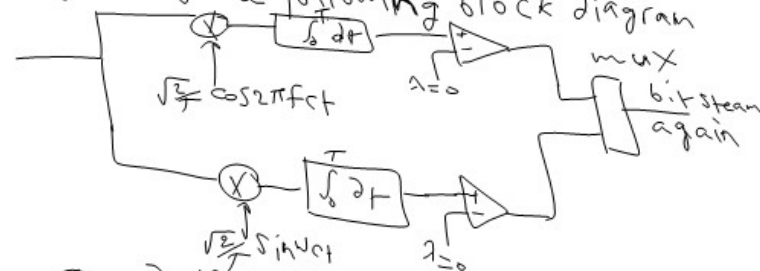
- * from the above equation we can see that the QPSK modulation is indeed a two BPSK systems

- * This means that the QPSK modulation can be presented by the following block diagram



QPSK demodulator

- * The QPSK demodulator can be implemented by using the following block diagram



Bandwidth of QPSK Signaling

- * The bandwidth for QPSK system is given by

$$B.W = \frac{1}{2} B.W_{BPSK} = \frac{1}{2} (2) R_b = R_b$$

A given communication system transmit informations at a bit rate of 1.75 Mbps. If the modulation scheme used is QPSK and the power spectral density for the noise is $N_0 = 1.26 \times 10^{-20}$ W/Hz

Determine the system transmission power

If the the average probability of error is not to exceed 1×10^{-7} and the path loss of the system is 144 dB including antenna gains

Solution

$$P_e = \frac{1}{2} \text{erfc} \sqrt{\frac{E_b}{N_0}}$$

$$1 \times 10^{-7} = \frac{1}{2} \text{erfc} \sqrt{\frac{P_r T_b}{N_0}}$$

from the erfc table, we have

$$\sqrt{\frac{P_r T_b}{N_0}} = 3.67$$

$$T_b = \frac{1}{R_b} = \frac{1}{1.75 \times 10^6}$$

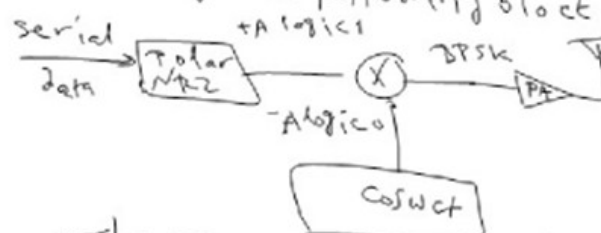
by solving the above square root, we have the received power

$$P_r =$$

Binary phase shift keying BPSK

* BPSK signaling is used in some wireless communication systems such as blue tooth and Zig-bee technology

* BPSK signals can be generated by using the following block diagram



* The BPSK signal might appear as shown below



* Note that the bit duration T_b is related to the carrier frequency by

$$f_c = \frac{n}{T_b}$$

where n is an integer number

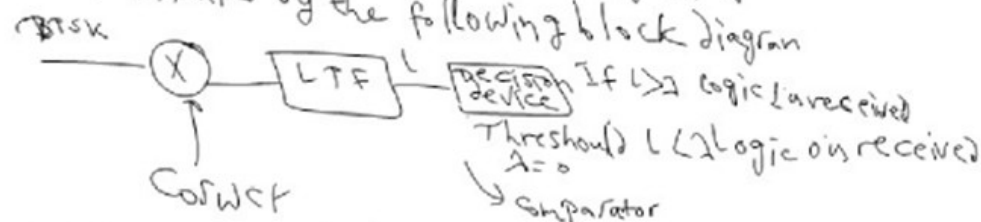
Note also logic 1 is represented by

$$s_1(t) = A \cos \omega t \quad \text{logic 1}$$

while logic zero is denoted by

$$s_2(t) = -A \cos \omega t = A \cos(\omega t + \pi) \quad \text{logic 0}$$

* The demodulator of BPSK signal is illustrated by the following block diagram



Note the BPSK demodulator is in fact a synchronous (coherent) detector, therefore it requires a synchronization circuit between the transmitter and the receiver.

Bandwidth of BPSK Signal

* The BW of BPSK signal is given by

$$BW = 2R_b$$

where R_b is the bit rate

Average probability of error of BPSK Signals

* The average probability of error is used to indicate how many bits are received correctly when we have a noisy communication channel

* The average probability of error for BPSK system is given mathematically by

$$P_e = \frac{1}{2} \operatorname{erfc}\left(\sqrt{\frac{E_b}{N_0}}\right)$$

where E_b is the energy per bit which is defined by $E_b = \frac{A^2}{2} T_b$

T_b is the bit duration

A is the amplitude of the carrier

N_0 is the power spectral density for the noise watt/Hz

erfc is the complementary error function which is given in tables

x	$\operatorname{erfc}(x)$
0.1	0.99

